

WHAT IS CLAIMED IS:

1. A thin film transistor array substrate for a liquid crystal display,
comprising:

an insulating substrate;

5 a gate line formed on the substrate;

a common electrode line proceeding parallel to the gate line;

a storage capacitor electrode connected to the common electrode line;

a gate insulating layer formed on the gate line, the common electrode
line, and the storage capacitor electrode;

10 a data line formed on the gate insulating layer;

a protective layer formed on the data line; and

a pixel electrode formed on the protective layer with opening patterns;

wherein the pixel electrode completely cover the storage capacitor
electrodes at particular regions.

15 2. The thin film transistor array substrate of claim 1, wherein the
storage capacitor electrode is respectively provided at left side and right side of
each pixel electrode.

3. The thin film transistor array substrate of claim 1, wherein the
common electrode line comprises two separate lines.

20 4. The thin film transistor array substrate of claim 1, wherein each
opening pattern comprises a horizontal opening portion formed at the boundary
of the pixel electrode bisecting the pixel electrode into an upper region and a
lower region, and inclined opening portions formed at the upper region and the

lower region of the pixel electrode while proceeding perpendicular to each other.

5. A thin film transistor array substrate, comprising:

an insulating substrate;

5 a common electrode line with branched electrodes, said common electrode line being formed on the insulating substrate; and

a pixel electrode insulated from the common electrode line and having first opening patterns,

10 wherein the pixel electrodes completely cover the branched electrodes of the common electrode lines at particular regions when viewed from the top side.

6. A liquid crystal display, comprising:

a bottom insulating substrate;

15 a common electrode line with branched electrodes, said common electrode line being formed on the bottom insulating substrate;

a pixel electrode insulated from the common electrode line and having first opening patterns, the pixel electrodes completely covering the branched electrodes of the common electrode line at particular regions when viewed from the top side;

20 a top insulating substrate facing the bottom substrate; and

a common electrode formed on the top substrate with second opening patterns,

wherein the second opening patterns are overlapped with sides of the pixel electrode at the particular regions where the pixel electrode completely cover the branched electrodes of the common electrode lines.

7. The liquid crystal display of claim 6, wherein the branched electrodes are respectively provided at a left side and a right side of each pixel electrode.

8. The liquid crystal display of claim 6, wherein the common electrode line comprises two separate lines.

9. The liquid crystal display of claim 6, wherein each first opening pattern comprises a horizontal opening portion formed at the boundary of the pixel electrode bisecting the pixel electrode into an upper region and a lower region, with inclined opening portions formed at the upper region and the lower regions of the pixel electrode while proceeding perpendicular to each other, each second opening pattern comprising inclined opening portions externally proceeding parallel to the upper and lower inclined opening portions of the pixel electrode, and linear opening portions bent from the inclined opening portions while being overlapped with the sides of the pixel electrode.

10. The liquid crystal display of claim 9, wherein the linear opening portions of the second opening pattern are overlapped with the vertical sides of the pixel electrode at the particular regions where the pixel electrode completely covers the branched electrodes of the common electrode lines.